



US009543453B2

(12) **United States Patent**  
**Arai**

(10) **Patent No.:** US 9,543,453 B2  
(45) **Date of Patent:** Jan. 10, 2017

(54) **SEMICONDUCTOR DEVICE INCLUDING JUNCTION FIELD EFFECT TRANSISTOR AND METHOD OF MANUFACTURING THE SAME**

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(\*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 52 days.

(21) Appl. No.: **14/469,799**

(22) Filed: **Aug. 27, 2014**

(65) **Prior Publication Data**

US 2015/0214385 A1 Jul. 30, 2015

(30) **Foreign Application Priority Data**

Jan. 24, 2014 (JP) ..... 2014-010876

(51) **Int. Cl.**

*H01L 29/808* (2006.01)  
*H01L 29/66* (2006.01)  
*H01L 29/10* (2006.01)  
*H01L 29/16* (2006.01)  
*H01L 21/04* (2006.01)

(52) **U.S. Cl.**

CPC ..... *H01L 29/8083* (2013.01); *H01L 21/046* (2013.01); *H01L 29/1066* (2013.01); *H01L 29/1608* (2013.01); *H01L 29/66068* (2013.01); *H01L 29/1058* (2013.01); *H01L 29/66909* (2013.01)

(58) **Field of Classification Search**

CPC ..... H01L 29/66909  
See application file for complete search history.

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(57) **ABSTRACT**

An on-resistance of a junction FET is reduced. In a semiconductor device in an embodiment, a gate region of the junction field effect transistor includes a low concentration gate region and a high concentration gate region whose impurity concentration is higher than an impurity concentration of the low concentration gate region, and the high concentration gate region is included in the low concentration gate region.

**11 Claims, 16 Drawing Sheets**

